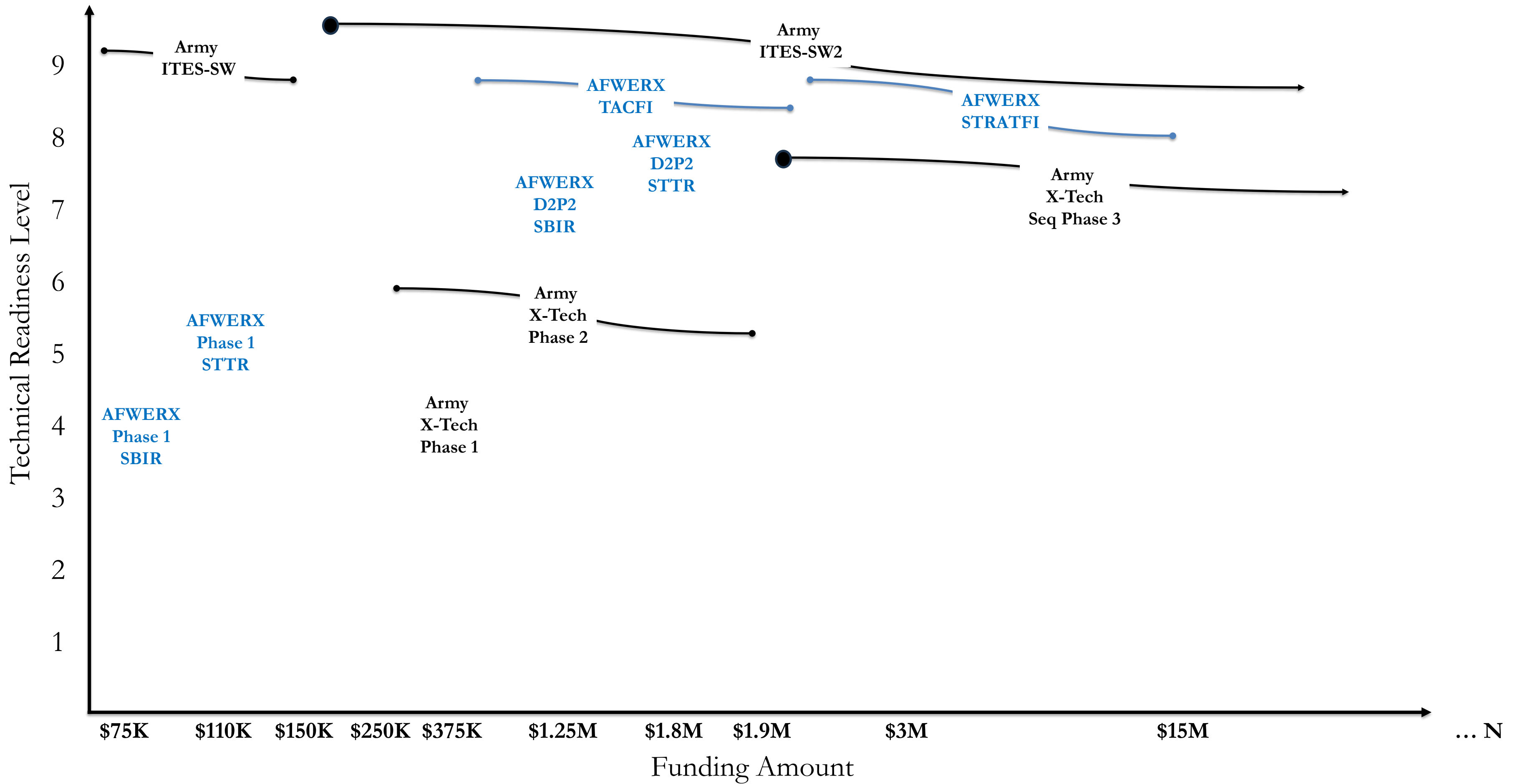


J. A. GREEN
& COMPANY

Federal Funding Programs

Overview of the Leading Funding Tools

Overview





Contact



Army

- Visit www.xtech.army.mil.
 - Click ‘Competitions’ to see active challenges.
- Register for SBIR/STTR notifications at www.armysbir.army.mil

Navy

- Contact Nick Kasatkin at the Navy’s Shipbuilding Industrial Base Task Force (SIB-TF) to enroll for their newsletter.
nick.kasatkin.ctr@us.navy.mil or
nick.kasatkin@tmbhq.com
- Register with NAVALX at www.secnav.navy.mil/agility/Pages/join-our-network/default.aspx.
 - Subscribe to announcements on the same page.

Air and Space Force

- Visit afwerxchallenge.com
- Register and enroll for SBIR/STTR notifications.

Technical Readiness Levels

9. Actual system proven through successful mission operations. Actual application of the technology in its final form and under mission conditions, such as those encountered in operational test and evaluation. Examples include using the system under operational mission conditions.

8. Actual system completed and qualified through test and demonstration. Technology has been proven to work in its final form and under expected conditions. In almost all cases, this TRL represents the end of true system development. Examples include developmental test and evaluation of the system in its intended weapon system to determine if it meets design specifications.

7. System prototype demonstration in an operational environment. Prototype near, or at, planned operational system. Represents a major step up from TRL 6, requiring demonstration of an actual system prototype in an operational environment such as an aircraft, vehicle, or space. Examples include testing the prototype in a test bed aircraft.

6. System/subsystem model or prototype demonstration in a relevant environment. Representative model or prototype system, which is well beyond that of TRL 5, is tested in a relevant environment. Represents a major step up in a technology's demonstrated readiness. Examples include testing a prototype in a high-fidelity laboratory environment or in simulated operational environment.

5. Component and/or breadboard validation in relevant environment. Fidelity of breadboard technology increases significantly. The basic technological components are integrated with reasonably realistic supporting elements so it can be tested in a simulated environment. Examples include "high fidelity" laboratory integration of components.

4. Component and/or breadboard validation in laboratory Environment. Basic technological components are integrated to establish that they will work together. This is relatively "low fidelity" compared to the eventual system. Examples include integration of "ad hoc" hardware in the laboratory.

3. Analytical and experimental critical function and/or characteristic proof of concept. Active research and development is initiated. This includes analytical studies and laboratory studies to physically validate analytical predictions of separate elements of the technology. Examples include components that are not yet integrated or representative.

2. Technology concept and/or application formulated. Invention begins. Once basic principles are observed, practical applications can be invented. Applications are speculative and there may be no proof or detailed analysis to support the assumptions. Examples are limited to analytic studies.

1. Basic principles observed and reported. Lowest level of technology readiness. Scientific research begins to be translated into applied research and development. Examples might include paper studies of a technology's basic properties.

